

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L34	0	(phase control circuit AND first flip-flop AND circuit under test AND second flip-flop AND first selector AND second selector AND semiconductor integrated circuit AND comparator).clm.	US-PPGPUB, USPAT	ADJ	ON	2006/06/25 15:54

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6303	phase:control circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:32
L2	5322	phase control signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:32
L3	680	phase difference same (first clock\$1 same second clock\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:32
L5	23155	resultant signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:33
L6	479	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:33
L7	1	l6 and l3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:34
L8	23	l3 and l5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:33
L9	9	l8 and (l1 or l2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:34

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L11	8682	second flip-flop or second FF	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:34
L15	377199	FF or flip flop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:36
L16	4	I15 and I9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:36
L17	2372180	device under test or unit under test or circuit under test or machine under test or DUT or UUT or CUT or MUT	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:37
L19	11423	evaluation circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:37
L20	28550	test circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:38
L21	2404157	I17 or I19 or I20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:38
L22	1	I21 and I16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:38

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L23	15577	714/724 or 714/709 or 714/707 or 714/731 or 714/740 or 714/744 or 714/814 or 714/799 or 714/736 or 714/798 or 714/699 or 714/700 or 324/763 or 324/765 or 702/72 or 702/66 or 713/400 or 375/216 or 375/375	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:47
L24	1	I23 and I16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:47
L25	10857	first flip flop or first FF	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 14:59
L26	1	I9 and I23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:34
L27	5044	I21 and I23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:08
L28	1	I27 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:08
L29	6	"6114890"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:47
L32	1872	326/93 or 326/96 or 327/107 or 327/166 or 327/170	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:49

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L33	3	I29 and I32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/25 15:51
L34	0	(phase control circuit AND first flip-flop AND circuit under test AND second flip-flop AND first selector AND second selector AND semiconductor integrated circuit AND comparator).clm	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:54

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L33	3	I29 and I32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	ADJ	ON	2006/06/25 15:51
L34	0	(phase control circuit AND first flip-flop AND circuit under test AND second flip-flop AND first selector AND second selector AND semiconductor integrated circuit AND comparator).clm.	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:55
L35	0	I32 and I16	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:56
L36	30	I32 and I27	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:56
L37	0	I36 and I9	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:57
L38	6	I36 and I15	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:58
L39	0	I38 and (I1 or I2 or I3 or I5)	US-PGPUB; USPAT	ADJ	ON	2006/06/25 15:57